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silicon germanium finfet device physics, process integration and modeling considerations (invited) darsen lua,pierre morinb, bhagawan sahuc, terence b. hooka, pouya ...

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silicon analog components device design, process integration, characterization, and reliability. authors: el-kareh, badih, hutter, lou n.

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silicon devices and process integration deep submicron and nano-scale technologies

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process for integration of energetic porous silicon devices matthew h ervin, brian isaacson and louis b levine . us army research laboratory, 2800 powder mill road ...

MOBILE DEVICE PASSIVE INTEGRATION FROM WAFER PROCESS

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mobile device passive integration from wafer process ... (integrated passive device process) and integration schemes ... mobile device passive integration from wafer ...

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monolithic silicon microphotronics: devices, process integration and circuits lionel c. kimerling

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process technology for silicon carbide devices ... technology for silicon carbide devices actually an alternative title might have been process integration ...

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